

IN THE CLAIMS

Please amend the claims as follows:

1. (Previously Presented): An information processing system comprising:

a first processor having a first local memory;

a second processor having a second local memory;

a third processor having a third local memory;

a shared memory shared by the first to third processors, the shared memory and the first to third local memories being mapped in a real address space;

means for mapping one of the second local memory and the third local memory in part of an effective address space of a first thread executed by the first processor, said one of the second local memory and the third local memory being the local memory of a corresponding one of the second processor and the third processor, which executes a second thread interacting with the first thread, the second thread holding thread context including contents of said one of the second local memory and the third local memory, the first thread accessing said one of the second local memory and the third local memory via said part of the effective address space of the first thread to interact with the second thread; and

means for mapping the other of the second local memory and the third local memory in said part of the effective address space of the first thread instead of said one of the second local memory and the third local memory when a processor that executes the second thread is changed from said one of the second processor and the third processor to the other of the second processor and the third processor, the first thread accessing the other of the second local memory and the third local memory via said part of the effective address space of the first thread to interact with the second thread.

2. (Previously Presented): The information processing system according to claim 1, further comprising:

means for storing contents of said one of the second local memory and the third local memory in a memory area on the shared memory when the second thread stops to run; and

means for mapping the memory area on the shared memory in said part of the effective address space of the first thread instead of said one of the second local memory and the third local memory, the first thread accessing the memory area on the shared memory via said part of the effective address space of the first thread to interact with the second thread.

3. (Previously Presented): The information processing system according to claim 2, further comprising:

means for restoring contents of the memory area on the shared memory to said one of the second local memory and the third local memory when the second thread is resumed by said one of the second processor and the third processor; and

means for mapping said one of the second local memory and the third local memory in said part of the effective address space of the first thread instead of the memory area on the shared memory.

4. (Previously Presented): The information processing system according to claim 2, further comprising:

means for restoring contents of the memory area on the shared memory to the other of the second local memory and the third local memory when the second thread is resumed by the other of the second processor and the third processor; and

means for mapping the other of the second local memory and the third local memory in said part of the effective address space of the first thread instead of the memory area on the shared memory.

5. (Previously Presented): A method of managing a local memory used for communication between a plurality of threads, the threads being executed by an information processing system including a first processor having a first local memory, a second processor having a second local memory, a third processor having a third local memory, and a shared memory shared by the first to third processors, the shared memory and the first to third local memories being mapped in a real address space, the method comprising:

mapping one of the second local memory and the third local memory in part of an effective address space of a first thread executed by the first processor, said one of the second local memory and the third local memory being the local memory of a corresponding one of the second processor and the third processor, which executes a second thread interacting with the first thread, the second thread holding thread context including contents of said one of the second local memory and the third local memory, the first thread accessing said one of the second local memory and the third local memory via said part of the effective address space of the first thread to interact with the second thread; and

mapping the other of the second local memory and the third local memory in said part of the effective address space of the first thread instead of said one of the second local memory and the third local memory when a processor that executes the second thread is changed from said one of the second processor and the third processor to the other of the second processor and the third processor, the first thread accessing the other of the second local memory and the third local memory via said part of the effective address space of the first thread to interact with the second thread.

6. (Previously Presented): The method according to claim 5, further comprising:
storing contents of said one of the second local memory and the third local memory in
a memory area on the shared memory when the second thread stops to run; and
mapping the memory area on the shared memory in said part of the effective address
space of the first thread instead of said one of the second local memory and the third local
memory, the first thread accessing the memory area on the shared memory via said part of the
effective address space of the first thread to interact with the second thread.

7. (Previously Presented): The method according to claim 6, further comprising:
restoring contents of the memory area on the shared memory to said one of the second
local memory and the third local memory, when the second thread is resumed by said one of
the second processor and the third processor; and
mapping said one of the second local memory and the third local memory in said part
of the effective address space of the first thread instead of the memory area on the shared
memory.

8. (Previously Presented): The method according to claim 6, further comprising:
restoring contents of the memory area on the shared memory to the other of the
second local memory and the third local memory, when the second thread is resumed by the
other of the second processor and the third processor; and
mapping the other of the second local memory and the third local memory in said part
of the effective address space of the first thread instead of the memory area on the shared
memory.

9. (Previously Presented): A program stored in computer-readable media, which causes a computer to manage a local memory used for communication between a plurality of threads, the computer including a first processor having a first local memory, a second processor having a second local memory, a third processor having a third local memory, and a shared memory shared by the first to third processors, the shared memory and the first to third local memories being mapped in a real address space, the program comprising:

causing the computer to map one of the second local memory and the third local memory in part of an effective address space of a first thread executed by the first processor, said one of the second local memory and the third local memory being the local memory of a corresponding one of the second processor and the third processor, which executes a second thread interacting with the first thread, the second thread holding thread context including contents of said one of the second local memory and the third local memory, the first thread accessing said one of the second local memory and the third local memory via said part of the effective address space of the first thread to interact with the second thread; and

causing the computer to map the other of the second local memory and the third local memory in said part of the effective address space of the first thread instead of said one of the second local memory and the third local memory when a processor that executes the second thread is changed from said one of the second processor and the third processor to the other of the second processor and the third processor, the first thread accessing the other of the second local memory and the third local memory via said part of the effective address space of the first thread to interact with the second thread.

10. (Previously Presented): The program according to claim 9, further comprising:

causing the computer to store contents of said one of the second local memory and the third local memory in a memory area on the shared memory when the second thread stops to run; and

causing the computer to map the memory area on the shared memory in said part of the effective address space of the first thread instead of said one of the second local memory and the third local memory, the first thread accessing the memory area on the shared memory via said part of the effective address space of the first thread to interact with the second thread.

11. (Currently Amended): The program according to claim 9, further comprising:

causing the computer to restore contents of ~~the~~ a memory area on the shared memory to said one of the second local memory and the third local memory, when the second thread is resumed by said one of the second processor and the third processor; and

causing the computer to map said one of the second local memory and the third local memory in said part of the effective address space of the first thread instead of the memory area on the shared memory.

12. (Currently Amended): The program according to claim 9, further comprising:

causing the computer to restore contents of ~~the~~ a memory area on the shared memory to the other of the second local memory and the third local memory, when the second thread is resumed by the other of the second processor and the third processor; and

causing the computer to map the other of the second local memory and the third local memory in said part of the effective address space of the first thread instead of the memory area on the shared memory.